

FIG. 1

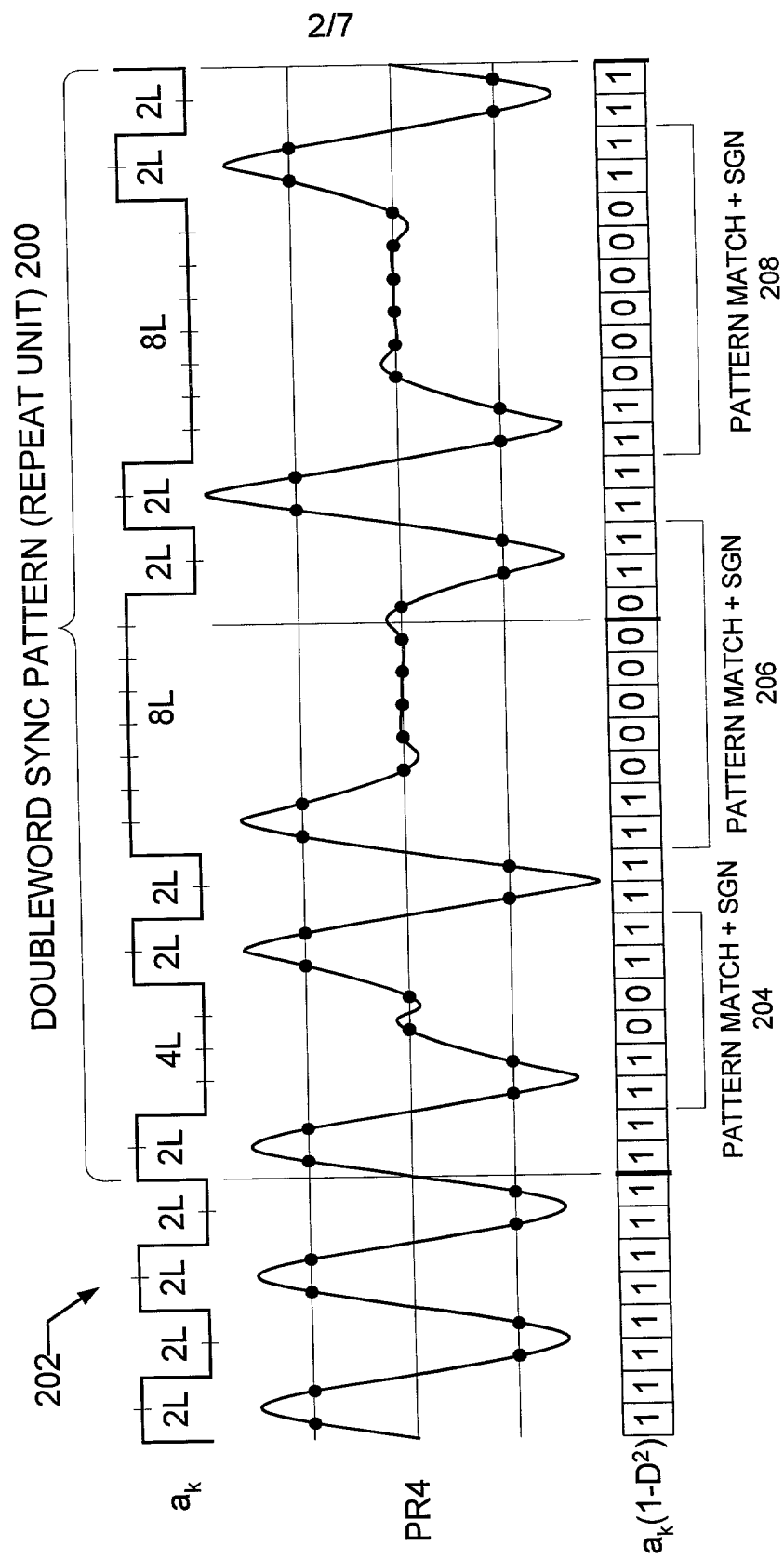


FIG. 2

FIG. 3A and FIG. 3B are block diagrams of a 3/7 convolutional encoder.

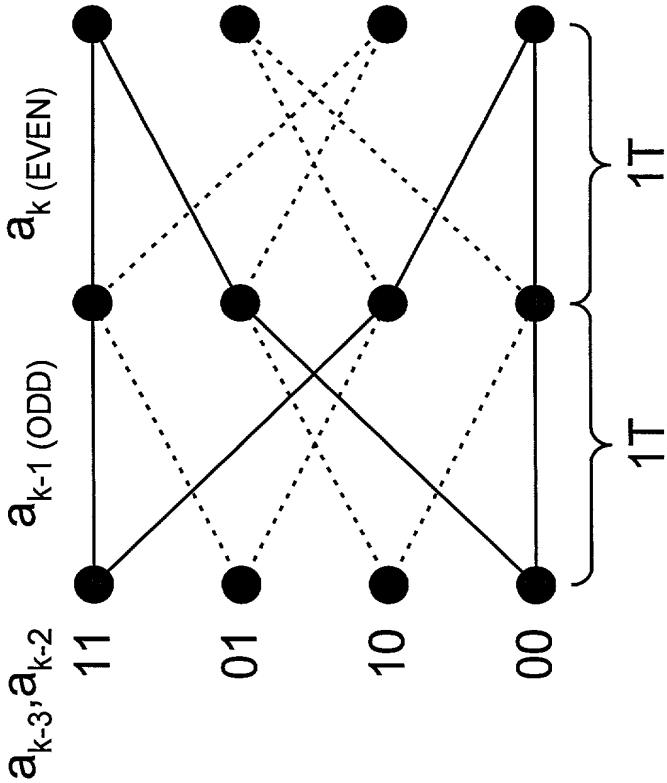


FIG. 3A

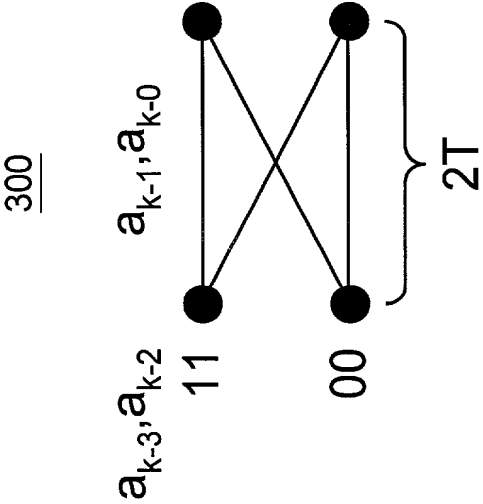


FIG. 3B

ROC920010065US1

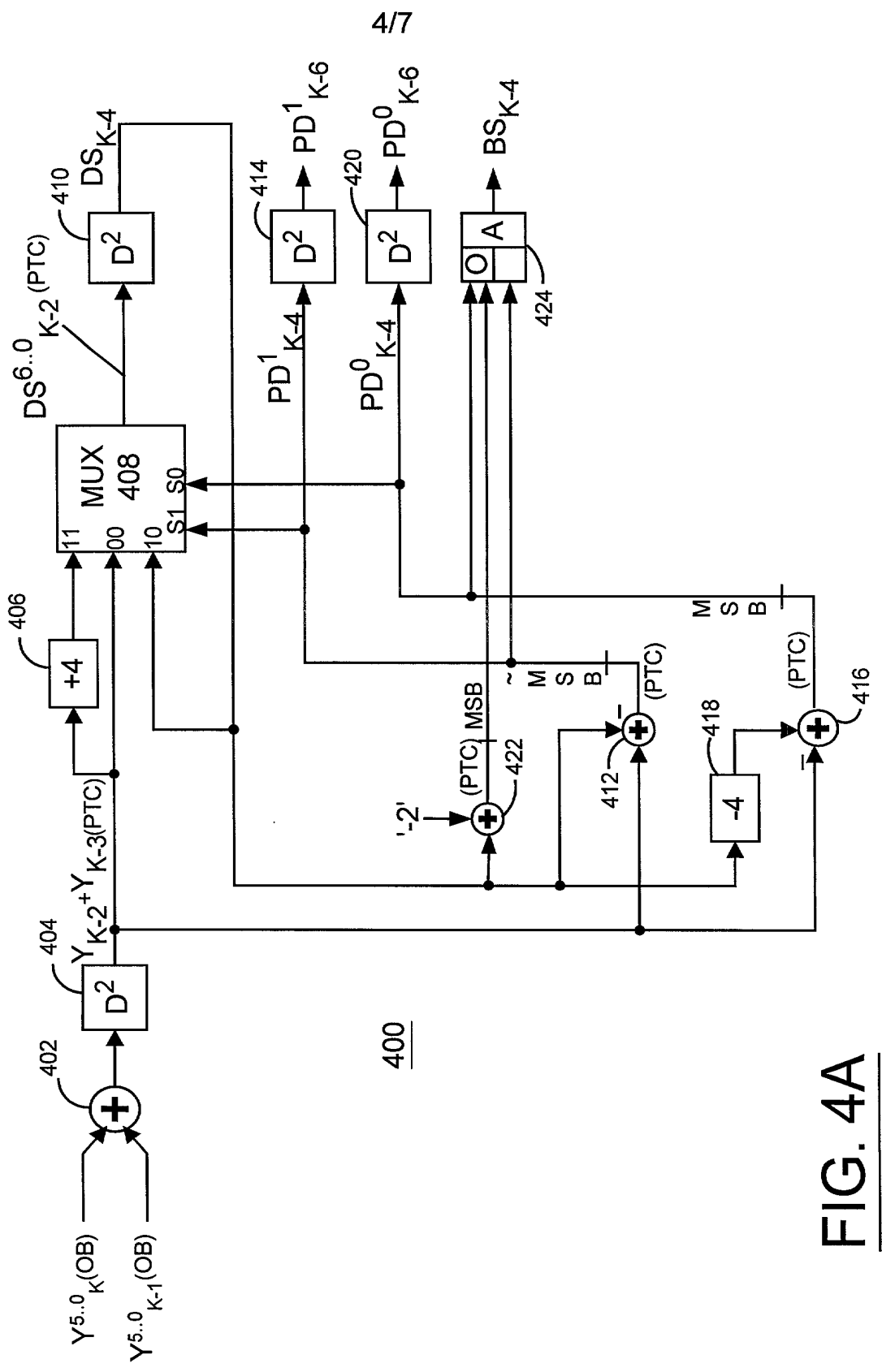


FIG. 4A

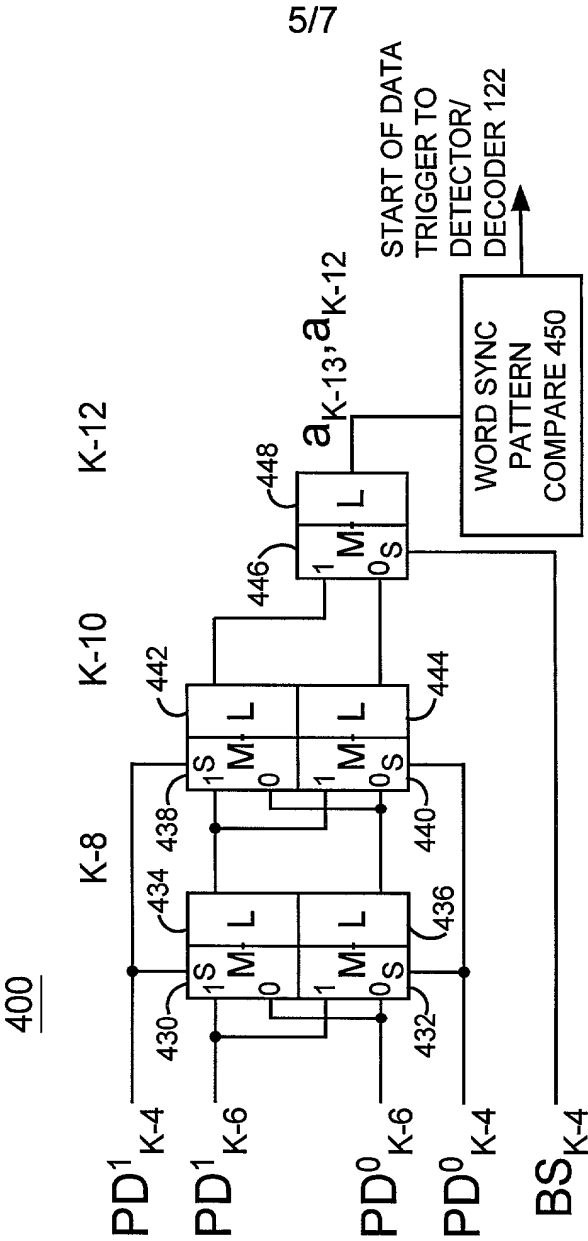


FIG. 4B

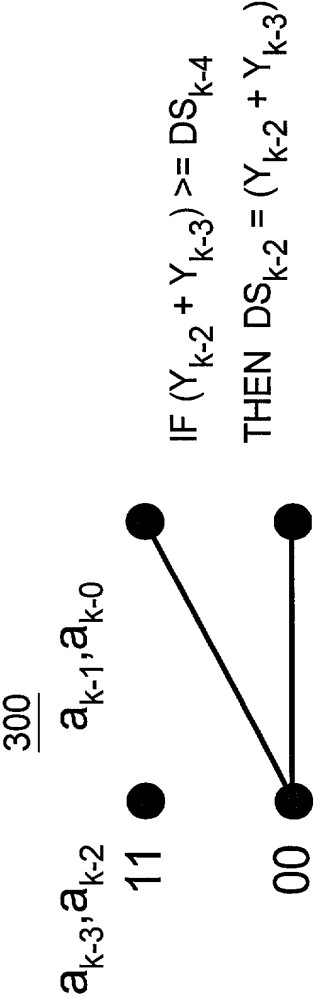


FIG. 5A

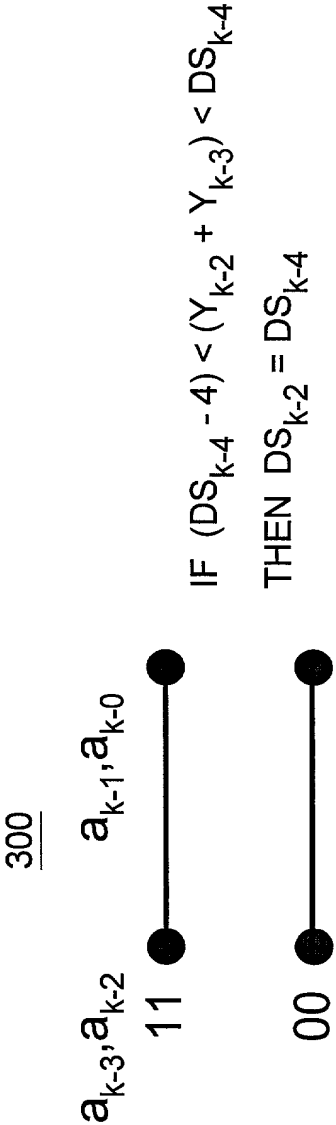


FIG. 5B

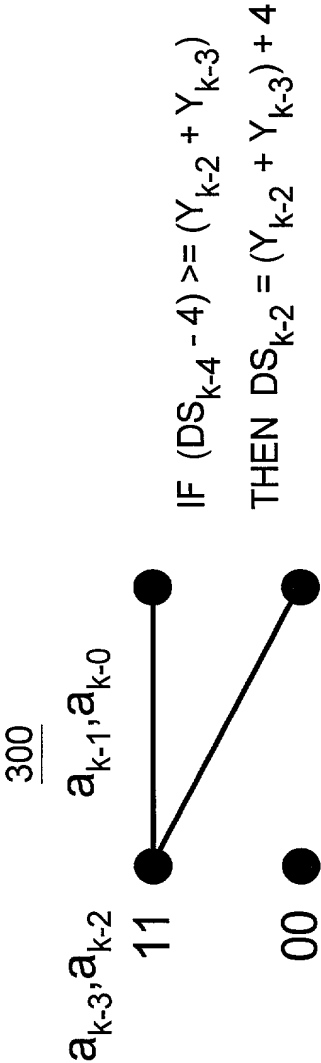


FIG. 5C

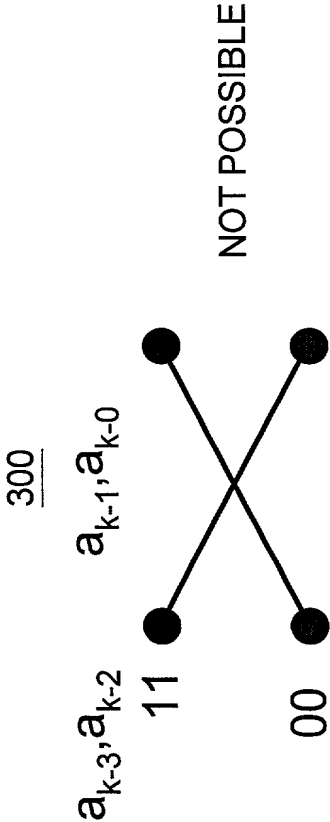


FIG. 5D